

**IN THE CLAIMS:**

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claims 1-21 and ADD new claim 22 in accordance with the following:

1. (CURRENTLY AMENDED) A method for driving a plasma display panel, wherein a display field, ~~corresponding to a display of a screen,~~ comprises a plurality of successive display subfields producing a gradation display, each display subfield comprises at least an address period to write cells to be lit in the display subfield in accordance with corresponding display data and a sustain period to cause light emission to occur in the written cells, said method comprising:

~~, a gradation display is realized combining a combination of the subfields to be among the plurality of subfields, and writing, in the address period of a selected one of the plurality of successive display subfields in each display field, all of the cells to be written in the respective address periods of all of the plurality of successive display subfields in the display field, are written; and~~

applying sustain pulses to cause light emission in the respective sustain periods of the successive display subfields ~~a predetermined subfield among the plurality of subfields making up of the display field.~~

2. (CURRENTLY AMENDED) A The method for driving a plasma display panel, as set forth in claim 1, wherein the ~~predetermined selected display~~ subfield is a the display subfield with the lowest luminance ratio.

3. (CURRENTLY AMENDED) A The method for driving a plasma display panel, as set forth in claim 1, wherein a display field has a display subfield with a same luminance ratio as that of the ~~predetermined selected display~~ subfield, in addition to the ~~predetermined selected display~~ subfield.

4. (CURRENTLY AMENDED) A The method for driving a plasma display panel, as

set forth in claim 1, wherein the ~~predetermined~~selected display subfield is the display subfield at the head in a display field.

5. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein an all-cell write discharge is caused to occur in the ~~predetermined~~selected display subfield before the address period.

6. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein an all-cell write discharge is caused to occur in the ~~predetermined~~selected display subfield and a display subfield with a heavy weight of luminance before the address period.

7. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 5, wherein the all-cell write discharge is caused to occur twice, successively, in the ~~predetermined~~selected subfield.

8. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 5, wherein a display subfield reset discharge is caused to occur in order to erase the residual charges in a lit cell in the display subfield immediately before the display subfield in which the all-cell write discharge is caused to occur.

9. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein the display subfield with the lowest luminance ratio is arranged at the head in a display field and the ~~predetermined~~selected display subfield is arranged in the second position in the display field.

10. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 9, wherein the ~~predetermined~~selected display subfield is one with the second lowest luminance ratio.

11. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 9, wherein an all-cell write discharge is caused to occur in the subfield at the head and the ~~predetermined~~selected display subfield before the address period.

12. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 9, wherein a display subfield reset discharge is caused to occur in order to erase the residual charges in a lit cell in the display subfield at the head.

13. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein the widths of an address pulse and a scan pulse during the address period in the ~~predetermined~~-selected display subfield are wider than those of the address pulse and the scan pulse during the address period in other display subfields.

14. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein the voltage of an address pulse during the address period in the ~~predetermined~~-selected display subfield is greater than that of the address pulse during the address period in other display subfields.

15. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein the voltage of a scan pulse during the address period in the ~~predetermined~~-selected display subfield is greater than that of the scan pulse during the address period in other display subfields.

16. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 1, wherein a process to suppress a discharge in an unlit cell is performed between the address period and the sustain period in the ~~predetermined~~-selected display subfield.

17. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 16, wherein the process to suppress a discharge in an unlit cell is a process in which, at the same time an address pulse is applied to an address electrode, a pulse, the applied voltage of which varies as time elapses, is applied to a scan electrode.

18. (CURRENTLY AMENDED) A-The method for driving a plasma display panel, as set forth in claim 17, wherein the final potential of the pulse, the applied voltage of which varies as time elapses, is lower than the finally reached potential of a charge control pulse, which is applied after an all-cell write discharge and the applied voltage of which varies as time elapses.

19. (CURRENTLY AMENDED) A The method for driving a plasma display panel, as set forth in claim 1, wherein the gradation display level is determined with the luminance due to lighting in the ~~predetermined~~ selected display subfield being taken into consideration.

20. (CURRENTLY AMENDED) A The plasma display device comprising a plasma display panel and a driving circuit for the plasma display panel, wherein the driving circuit drives the plasma display panel using the driving methods set forth in claim 1.

21. (CURRENTLY AMENDED) A method for driving a plasma display panel, wherein a display field, ~~corresponding to a display of a screen,~~ comprises a plurality of successive display subfields producing a gradation display, each display subfield comprises at least an address period to write cells to be lit in the display subfield in accordance with corresponding display data and a sustain period to cause light emission to occur in the written cells, said method comprising:

~~a gradation display is realized by combining subfields to be lit among the plurality of subfields, and~~ writing, in the address period of a first display subfield and/or a second display subfield among the plurality of display subfields, all of the cells to be written in the respective address periods of ~~all of the~~ plurality of successive display subfields in the display field; and

~~applying sustain pulses are written to cause light emission in the respective sustain periods of the successive display subfields in a first subfield and/or a second subfield among the plurality of subfields making up of the display field.~~

22. (NEW) A method for driving a plasma display panel to produce a gradation display in a display field of a plurality of associated, successive subfields having respective address and sustain periods, comprising:

addressing, in the address period of an initial subfield, a composite of all cells to be selected, in accordance with display data, in the plurality of successive address periods respectively associated with the plurality of successive, associated subfields of the display field; and

applying sustain pulses to cause light emission in the respective sustain periods of the associated, successive subfields of the display field.